Figure 1

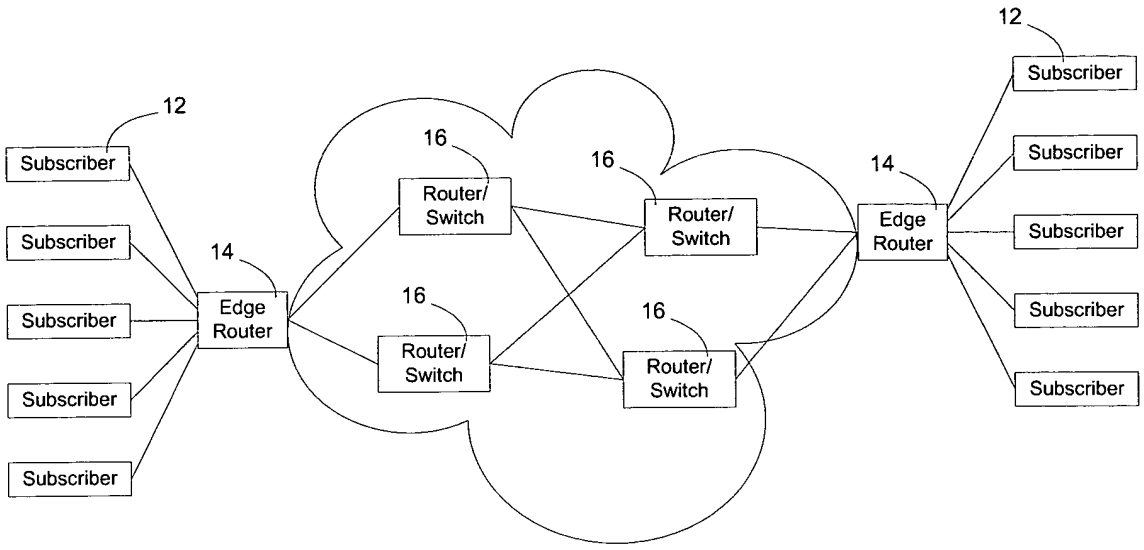

Figure 2

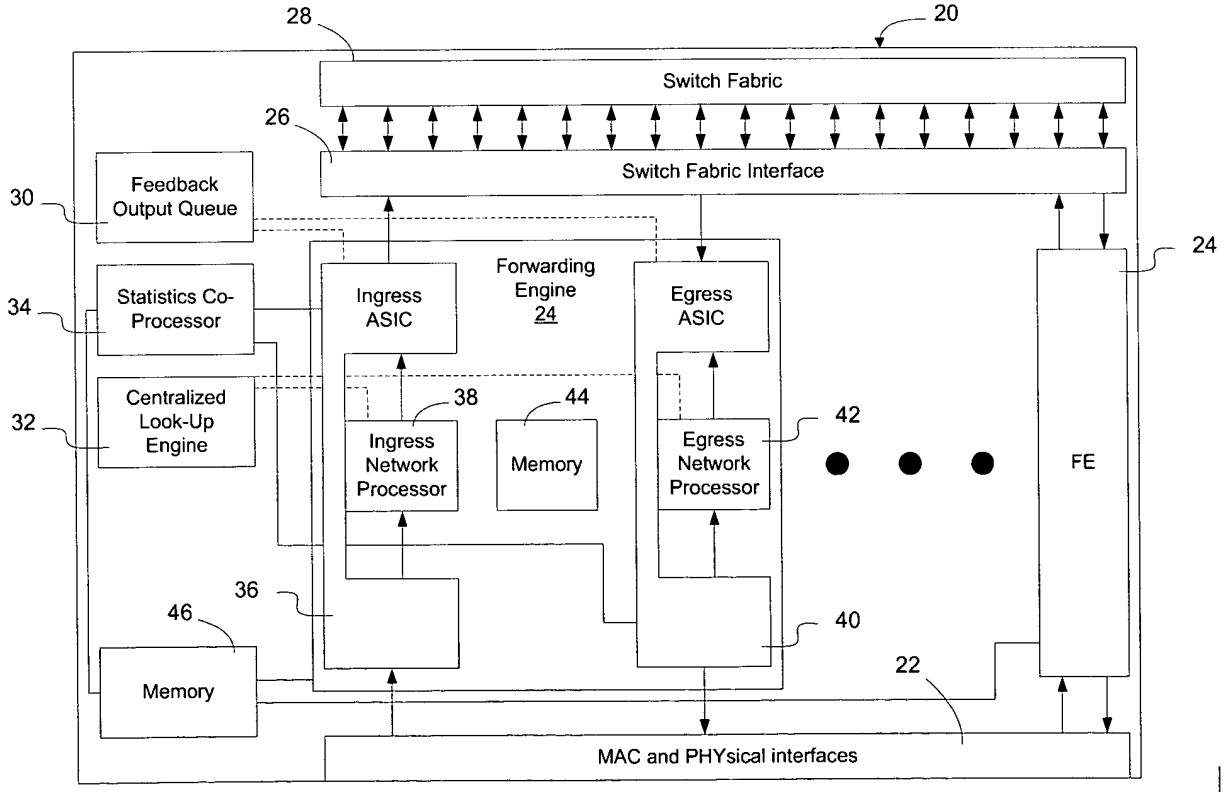

Figure 3

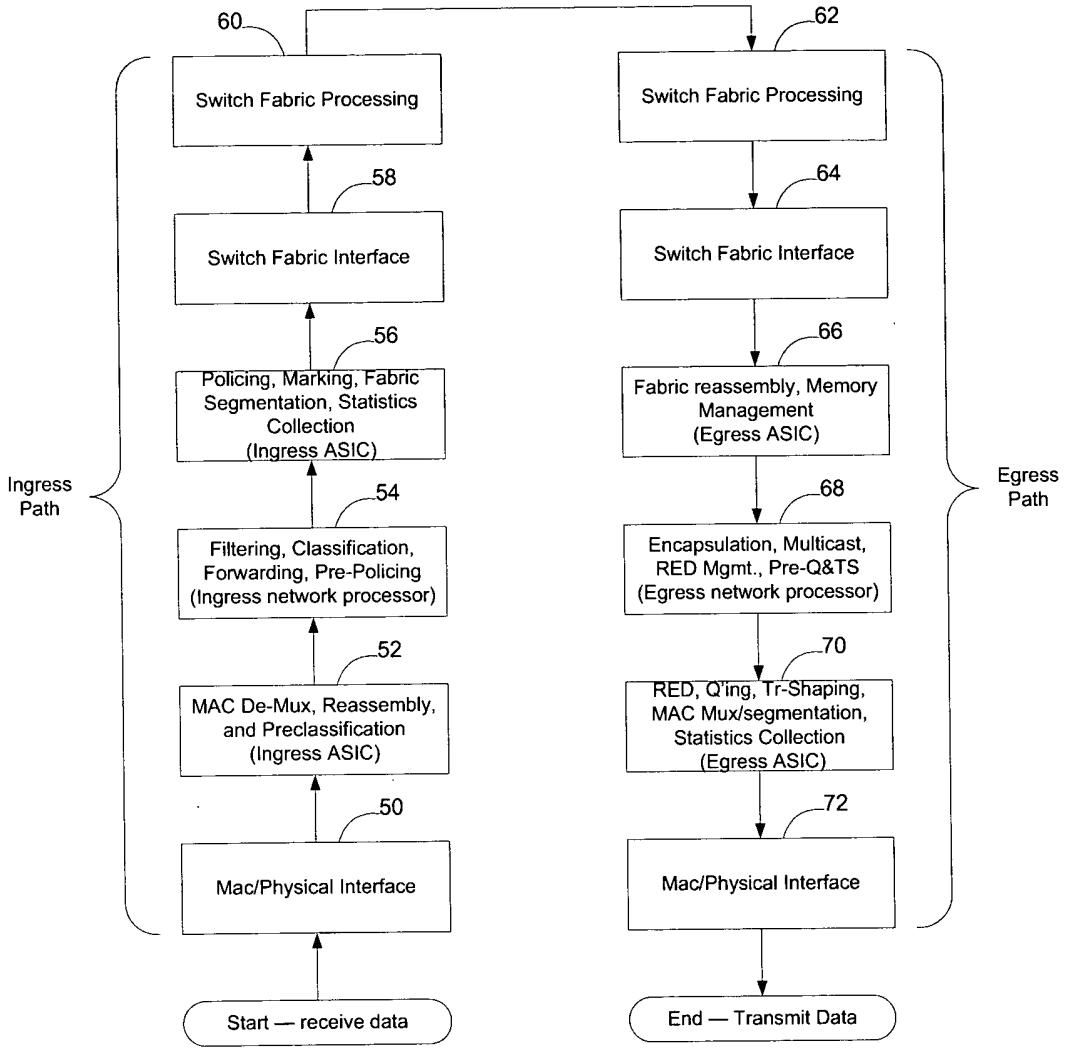
60
62
Switch Fabric Processing
Switch Fabric Processing
58
64
Switch Fabric Interface
Switch Fabric Interface
56
66
Policing, Marking, Fabric Segmentation, Statistics Collection (Ingress ASIC)
Fabric reassembly, Memory Management (Egress ASIC)
Ingress Path
54
68
Egress Path
Filtering, Classification, Forwarding, Pre-Policing (Ingress network processor)
Encapsulation, Multicast, RED Mgmt., Pre-Q&TS (Egress network processor)
52
70
MAC De-Mux, Reassembly, and Preclassification (Ingress ASIC)
RED, Q'ing, Tr-Shaping, MAC Mux/segmentation, Statistics Collection (Egress ASIC)
50
72
Mac/Physical Interface
Mac/Physical Interface
Start — receive data
End — Transmit Data

Figure 4

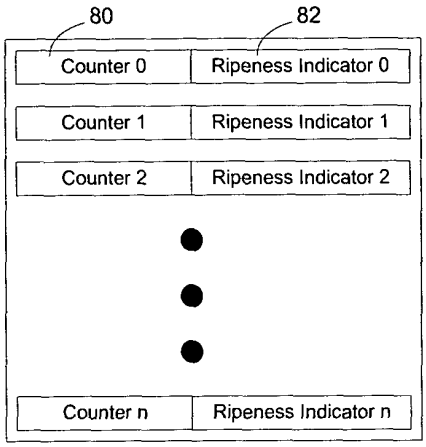

Figure 5

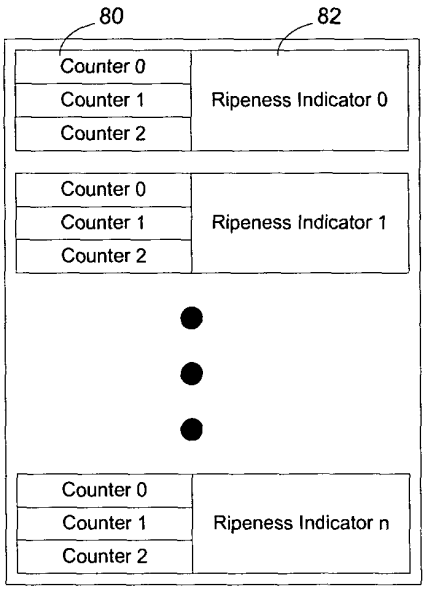

Figure 6

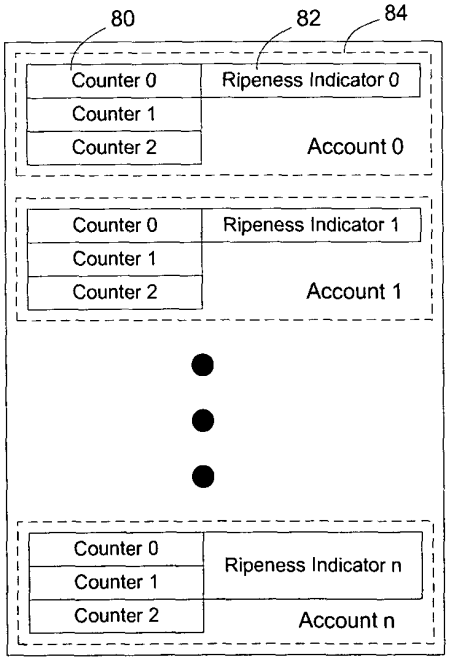

Figure 7

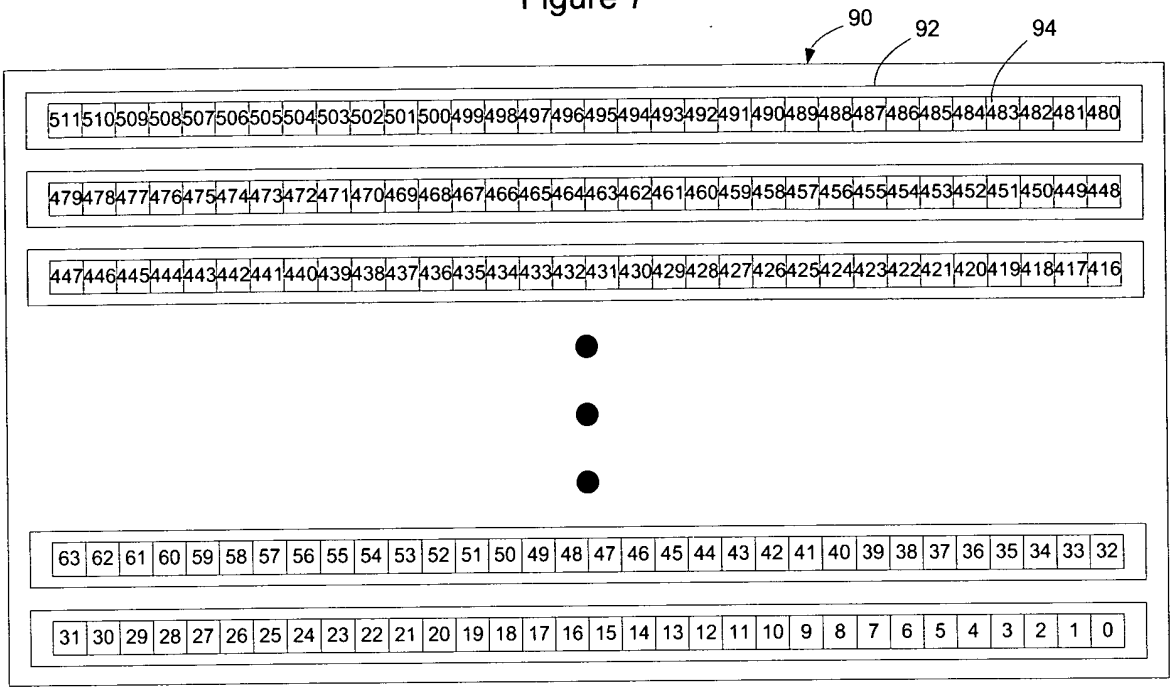
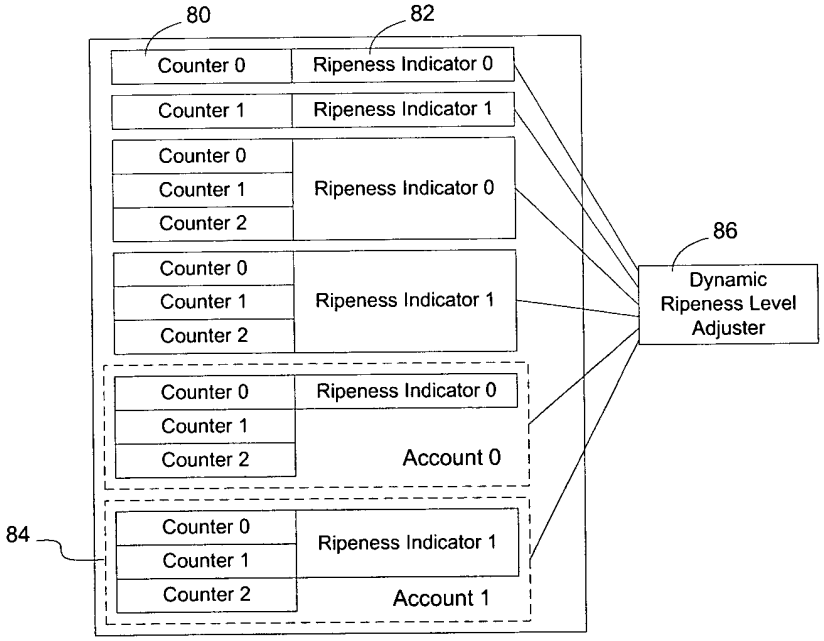
Figure 8